



UNITED STATES PATENT AND TRADEMARK OFFICE

cel

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/834,591	04/13/2001	Kenneth James Barker	RAL92000013US2	4261
25299	7590	06/30/2005	EXAMINER	
IBM CORPORATION PO BOX 12195 DEPT YXSA, BLDG 002 RESEARCH TRIANGLE PARK, NC 27709			CHANG, RICHARD	
			ART UNIT	PAPER NUMBER
			2663	

DATE MAILED: 06/30/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/834,591

Applicant(s)

BARKER ET AL.

Examiner

Richard Chang

Art Unit

2663

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 3/1/2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 2,3 and 5-11 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 9 is/are allowed.
- 6) ☒ Claim(s) 2,3,5-8 and 10 is/are rejected.
- 7) ☒ Claim(s) 11 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 04/18/2002 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Response to Amendment

1. Applicant's arguments with respect to claims 2-3 and 5-9 have been fully considered but are moot in view of the new ground(s) of rejection.

Claims 1 and 4 had been canceled.

Claim Rejections - 35 USC § 103

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

3. Claims 2-3, 5-8 and 10 are rejected under 35 U.S.C. 103(a) as being unpatentable over the admitted prior art in view of US patent 4,731,785 ("Ferenc et al.") and US patent 4,873,663 ("Baranyai et al.").

Regarding Claims 2, 5 and 6-7, The admitted prior art teaches a fast Ethernet system (2), which services 10 Ethernet networks (6) (for coupling a plurality of sources to a single processing chip) comprising of:

a multi-port physical layer (4) (a substrate including a system for processing Ethernet inputs),

a chip clock at a particular speed allowing each of the ports to be serviced during each of the clock cycles (a clock input for determining a time period for processing the input from a single source) (See Fig. 1, page 6, line 17 – page 7, line 13).

The admitted prior art lacks or does not disclose expressly the particular application involving limitation of

“a single input gate for a single input” and

“a sequencer which determines which source is being processed during the time period and which services a different source during the next clock period”.

Ferenc et al. teach apparatus and a method for transmitting both circuit switch and packet information over a common time division multiplexed digital path comprising of

a single common time division multiplexed (TDM) digital signal path conductor (109) (a single input gate for a single input) between transmitter (101) and receiver (121) (See Fig. 1, Col 4, lines 35-36), and

a circuit switch (102) (TDM sequencer) supplying necessary clock and timing signals to control multiplexer (105) (determines which source is being processed during the time period and which services a different source during the next clock period) (See Fig. 3, Col 6, lines 56-60).

A person of ordinary skill in the art would have been motivated to employ Ferenc et al. in the admitted prior art in order to obtain a fast Ethernet system servicing a plurality of Ethernet networks and to take advantage of using only a single TDM link and

the TDM sequencer to service different sources at different time period in claims 2, 5 and 6-7.

The suggestion/motivation to do so would have been to transmit data from different sources on a single TDM link, as suggested by Ferenc in Col. 1, line 64, to Col. 2, line 1. At the time the invention was made, therefore, it would have been obvious to one of ordinary skill in the art to which the invention pertains to combine Ferenc et al. with the admitted prior art to obtain the inventions specified in claims 2, 5 and 6-7.

The admitted prior art and Ferenc et al. lack or do not disclose expressly the particular application involving limitation of

"a signal for indicating when a given source is being processed".

Baranyai et al. teach a methods and apparatus for timing control the TDM network comprising a Frame Sync signal for indicating when a given source is being processed in the corresponding time slot relative to the Frame Sync signal wherein each clock period identifies a source sequentially (See Fig. 3, Col. 3, lines 40-47).

A person of ordinary skill in the art would have been motivated to employ Baranyai et al. in the admitted prior art and Ferenc et al. in order to obtain a fast Ethernet system servicing a plurality of Ethernet networks and to take advantage of using a Frame Sync signal for indicating when a given source is being processed in the corresponding time slot relative to the Frame Sync signal wherein each clock period identifies a source sequentially in claims 2, 5 and 6-7.

The suggestion/motivation to do so would have been to use a Frame Sync signal for indicating when a given source is being processed in the corresponding time slot

Art Unit: 2663

relative to the Frame Sync signal wherein each clock period identifies a source sequentially, as suggested by Baranyai et al. in Col. 3, lines 40-47. At the time the invention was made, therefore, it would have been obvious to one of ordinary skill in the art to which the invention pertains to combine Baranyai et al. with the admitted prior art and Ferenc et al. to obtain the inventions specified in claims 2, 5 and 6-7.

Regarding claim 3, 8 and 10, this claim have limitation that is similar to those of claim 3, 8 and 10, thus it is rejected with the same rationale applied against claim 3, 8 and 10 above.

Allowable Subject Matter

4. Claim 9 is allowed.

5. Claim 11 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims and if no art rejection can be applied.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Richard Chang whose telephone number is (571) 272-3129. The examiner can normally be reached on Monday - Friday from 8 AM to 5 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ricky Ngo can be reached on (571) 272-3139. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Art Unit: 2663

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

rk
rk

Richard Chang
Patent Examiner
Art Unit 2663

Ricky Ngo
RICKY NGO
PRIMARY EXAMINER

9/27/05